

Title (en)
Circuit arrangement for parameter adjustment

Title (de)
Schaltungsanordnung zur Parametereinstellung

Title (fr)
Dispositif de circuit pour ajustement de paramètres

Publication
EP 0807898 A3 19980617 (DE)

Application
EP 97107745 A 19970512

Priority
DE 19620033 A 19960517

Abstract (en)
[origin: EP0807898A2] Three analog multiplying stages (1-3) each consisting of a differential amplifier with emitter-coupled transistors (e.g. 4,5) receive respective input signals (17-19) at the base of one transistor of each pair while the base of the other is earthed (23). Another multiplying stage (25) is identically constructed to receive a first reference signal from a source (40) and potential divider (41,42). The output of this stage is compared (38) with a second reference signal (Us). The result of the comparison is used to derive the control signals for the current-bank output transistors (28-34) in the coupled emitter circuits of all stages.

IPC 1-7
G06G 7/16

IPC 8 full level
G06G 7/16 (2006.01)

CPC (source: EP US)
G06G 7/16 (2013.01 - EP US)

Citation (search report)
• [A] WO 9015397 A1 19901213 - SIEMENS AG [DE]
• [A] DE 2444007 A1 19750403 - PHILIPS NV

Designated contracting state (EPC)
DE DK FR GB IT

DOCDB simple family (publication)
EP 0807898 A2 19971119; EP 0807898 A3 19980617; EP 0807898 B1 20030129; DE 19620033 C1 19971211; DE 59709210 D1 20030306; US 5834963 A 19981110

DOCDB simple family (application)
EP 97107745 A 19970512; DE 19620033 A 19960517; DE 59709210 T 19970512; US 85881897 A 19970519