

Title (en)
Method and device for correcting synchronisation errors

Title (de)
Verfahren und Vorrichtung zur Synchronisierungsfehlerkorrektur

Title (fr)
Procédé et dispositif de correction d'erreur de synchronisation

Publication
EP 0808062 B1 20021023 (FR)

Application
EP 97401025 A 19970507

Priority
FR 9605961 A 19960514

Abstract (en)
[origin: EP0808062A1] The method employs a threshold detector (2) which delivers a composite synchronisation signal (Csync) to a digital calculator (4) which integrates (20) and separates (24) vertical and horizontal sync signals for the counters (8,9) of a line selection module (6). One counter (8) determines the vertical blanking interval line to be explored, and the other (9) determines the instant of a time window opening in that line. The outputs from a line decoder (22) and a multiplexer (7) driven by the respective counters are AND-gated (16).

IPC 1-7
H04N 7/035; **H04N 5/10**

IPC 8 full level
H04N 5/06 (2006.01); **H04N 5/10** (2006.01); **H04N 7/035** (2006.01); **H04N 7/083** (2006.01); **H04N 7/087** (2006.01); **H04N 7/088** (2006.01)

CPC (source: EP US)
H04N 5/10 (2013.01 - EP US); **H04N 7/035** (2013.01 - EP US); **H04N 7/088** (2013.01 - EP US)

Cited by
US7321397B2; US6972803B2; US7321398B2; WO2005025208A1

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EP 0808062 A1 19971119; **EP 0808062 B1 20021023**; CN 1105456 C 20030409; CN 1171697 A 19980128; DE 69716509 D1 20021128; DE 69716509 T2 20030626; FR 2748882 A1 19971121; FR 2748882 B1 19980626; JP 3986614 B2 20071003; JP H1056625 A 19980224; US 6130718 A 20001010

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