

Title (en)

Circuit arrangement for generating a resistance with adjustable positive temperature coefficient and the use of this circuit

Title (de)

Schaltungsanordnung zum Erzeugen eines Widerstandsverhaltens mit einstellbarem positiven Temperaturkoeffizienten, sowie Verwendung dieser Schaltungsanordnung

Title (fr)

Agencement de circuit pour la génération d'une résistance à coefficient de température positif et réglable ainsi que son utilisation

Publication

**EP 0810505 A3 19980422 (DE)**

Application

**EP 97108343 A 19970522**

Priority

DE 19621749 A 19960530

Abstract (en)

[origin: EP0810505A2] The circuit uses an ohmic resistor (1) in series with a diode element (3), with a second ohmic resistor (2) connected in parallel with the series circuit, having a resistance value corresponding to the required positive temp coefficient. Pref. the circuit is connected across the base-emitter path of a current mirror transistor (5) having an emitter resistance (4) of equal value to the first ohmic resistor.

IPC 1-7

**G05F 3/24**; **G05F 3/26**

IPC 8 full level

**G05F 3/22** (2006.01); **G05F 3/24** (2006.01); **G05F 3/26** (2006.01)

CPC (source: EP US)

**G05F 3/24** (2013.01 - EP US); **Y10S 323/907** (2013.01 - EP US)

Citation (search report)

- [A] US 4313082 A 19820126 - NEIDORFF ROBERT A
- [A] US 4956567 A 19900911 - HUNLEY STEVEN A [US], et al
- [A] EP 0492117 A2 19920701 - MOTOROLA INC [US]
- [A] US 3956661 A 19760511 - SAKAMOTO JYUNJI, et al

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

**EP 0810505 A2 19971203**; **EP 0810505 A3 19980422**; **EP 0810505 B1 19990728**; DE 19621749 A1 19971204; DE 19621749 C2 19980716; DE 59700279 D1 19990902; US 6121763 A 20000919

DOCDB simple family (application)

**EP 97108343 A 19970522**; DE 19621749 A 19960530; DE 59700279 T 19970522; US 86641597 A 19970530