

Title (en)

METHOD FOR ADDRESSING A FLAT SCREEN USING PIXEL PRECHARGING, DRIVER FOR CARRYING OUT THE METHOD, AND USE THEREOF IN LARGE SCREENS

Title (de)

FLACHSCHIRMDRESSIERUNGSVERFAHREN MIT BILDELEMENTVORLADUNG, STEUEREINRICHTUNG ZUR DURCHFÜHRUNG DES VERFAHRENS UND ANWENDUNG IN GROSSBILDSCHIRMEN

Title (fr)

PROCEDE D'ADRESSAGE D'UN ECRAN PLAT UTILISANT UNE PRECHARGE DES PIXELS, CIRCUIT DE COMMANDE PERMETTANT LA MISE EN OEUVRE DU PROCEDE ET SON APPLICATION AUX ECRANS DE GRANDES DIMENSIONS

Publication

EP 0815552 A1 19980107 (FR)

Application

EP 97900254 A 19970109

Priority

- FR 9700039 W 19970109
- FR 9600259 A 19960111

Abstract (en)

[origin: WO9725706A1] A method for addressing a flat screen consisting of rows and columns with pixels at the intersections thereof, wherein, at the start of each sampling of the video signal to be displayed on the screen, a voltage (Vr) higher than the useful voltage range (V) is applied to the selected pixel for a time tr, whereafter the useful voltage is sampled for a time ts. The invention is useful for LCD drivers.

IPC 1-7

G09G 3/36

IPC 8 full level

G02F 1/133 (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP KR US)

G09G 3/36 (2013.01 - KR); **G09G 3/3648** (2013.01 - EP US); **G09G 3/3688** (2013.01 - EP US); **G09G 2310/0205** (2013.01 - EP US); **G09G 2310/0248** (2013.01 - EP US); **G09G 2310/0251** (2013.01 - EP US); **G09G 2310/0297** (2013.01 - EP US); **G09G 2320/0204** (2013.01 - EP US)

Citation (search report)

See references of WO 9725706A1

Cited by

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DOCDB simple family (publication)

WO 9725706 A1 19970717; DE 69722309 D1 20030703; DE 69722309 T2 20040408; EP 0815552 A1 19980107; EP 0815552 B1 20030528; FR 2743658 A1 19970718; FR 2743658 B1 19980213; JP 4547047 B2 20100922; JP H11502325 A 19990223; KR 100445675 B1 20041208; KR 19980702958 A 19980905; US 6359608 B1 20020319

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