

Title (en)
Digital signal multiplexing apparatus

Title (de)
Digitale Multiplex-Vorrichtung

Title (fr)
Dispositif de multiplexage de signaux numériques

Publication
EP 0818900 A3 20000802 (EN)

Application
EP 97111462 A 19970707

Priority
JP 19523596 A 19960708

Abstract (en)
[origin: EP0818900A2] A digital signal multiplexing apparatus is provided which can multiplex service data having a plurality of different bit rates and change the bit rate even during signal transmission. A controller generates multiplex configuration information indicating how service data is multiplexed, in accordance with instructions, and generates commands whose contents match the multiplex configuration information. The generated multiplex configuration information is written in a memory circuit, and the generated command is converted by a CPU into a control signal for encoders. The multiplex configuration information read from the memory circuit is encoded by a convolution encoder into a predetermined format. A plurality of service data sets are encoded by convolution encoders into predetermined formats in accordance with control signals converted by CPU, and multiplexed by a multiplexer circuit. The data encoded by the convolution encoder and the data multiplexed by the multiplexer circuit are further multiplexed by another multiplexer circuit.

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H04J 3/16; H04H 1/00

IPC 8 full level
H04H 1/00 (2006.01); **H04J 3/04** (2006.01); **H04J 3/16** (2006.01); **H04N 7/173** (2006.01)

CPC (source: EP)
H04H 20/93 (2013.01); **H04H 60/07** (2013.01); **H04H 2201/20** (2013.01)

Citation (search report)
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• [A] US 5509013 A 19960416 - ADACHI MAKOTO [JP], et al
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