

Title (en)
Transmitting and receiving apparatus

Title (de)
Funksender-empfänger

Title (fr)
Emetteur-récepteur

Publication
EP 0823791 A3 20030129 (EN)

Application
EP 97305784 A 19970731

Priority
JP 20617496 A 19960805

Abstract (en)
[origin: EP0823791A2] A high frequency circuit of the single superheterodyne receiving system in the FDMA/TDD transmitting and receiving apparatus can be arranged so as to satisfy standards for spurious signals. The high frequency circuit block includes a receiving system and a transmitting system. A reception enable signal is supplied to the receiving system and a transmission enable signal is supplied to the transmitting system, so that a receiving channel and a transmitting channel are alternately switched. The transmission enable signal is supplied to the transmitting system via a rise delay circuit. The rise delay circuit is provided at an input side of the driver amplifier of the transmitting system.
<IMAGE>

IPC 1-7
H04B 1/56; **H04B 1/40**

IPC 8 full level
H04B 1/40 (2015.01); **H04J 1/00** (2006.01); **H04B 1/48** (2006.01); **H04B 1/56** (2006.01)

CPC (source: EP KR US)
H04B 1/405 (2013.01 - EP KR US); **H04B 1/48** (2013.01 - KR); **H04B 1/52** (2013.01 - KR); **H04B 1/48** (2013.01 - EP US);
H04B 1/56 (2013.01 - EP US)

Citation (search report)

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Designated contracting state (EPC)
AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

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EP 0823791 A2 19980211; **EP 0823791 A3 20030129**; JP H10107677 A 19980424; KR 19980018291 A 19980605; TW 399383 B 20000721;
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EP 97305784 A 19970731; JP 18661197 A 19970711; KR 19970036843 A 19970801; TW 86110906 A 19970730; US 90328797 A 19970730