

Title (en)

DEMODULATOR AND A METHOD OF DEMODULATION IN A TDM RECEIVER

Title (de)

DEMODULATOR UND DEMODULATIONSVERFAHREN IN EINEM TDM-EMPFÄNGER

Title (fr)

DEMULATEUR ET PROCEDE DE DEMODULATION DANS UN RECEPTEUR A MULTIPLEXAGE DANS LE TEMPS

Publication

**EP 0824816 A1 19980225 (EN)**

Application

**EP 96910120 A 19960419**

Priority

- GB 9600946 W 19960419
- GB 9509405 A 19950510

Abstract (en)

[origin: WO9636152A1] A demodulator for a receiver of digital data in packets sent in predetermined time slots within fixed length time frames is provided. The demodulator comprises at least one data processor and is operative to receive data packets each including synchronisation data. The demodulator includes a buffer for storing data of a received data packet, processing means for reading at least part of the synchronisation data both in the order the synchronisation data was received and in the reverse order, and training means for training the or each data processor dependent upon the synchronisation data.

IPC 1-7

**H04L 25/03**; H04J 3/06; H04L 27/22; H04L 27/38

IPC 8 full level

**H04L 27/22** (2006.01); **H04J 3/00** (2006.01); **H04J 3/06** (2006.01); **H04L 25/03** (2006.01)

CPC (source: EP)

**H04J 3/06** (2013.01); **H04L 25/03019** (2013.01); **H04L 2025/03662** (2013.01)

Citation (search report)

See references of WO 9636152A1

Designated contracting state (EPC)

AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)

**WO 9636152 A1 19961114**; AU 5342096 A 19961129; BR 9608198 A 19980721; EP 0824816 A1 19980225; GB 9509405 D0 19950705; IL 118019 A0 19960804; JP H11505086 A 19990511; MX 9708609 A 19980228; ZA 963518 B 19971103

DOCDB simple family (application)

**GB 9600946 W 19960419**; AU 5342096 A 19960419; BR 9608198 A 19961110; EP 96910120 A 19960419; GB 9509405 A 19950510; IL 11801996 A 19960423; JP 53385196 A 19960419; MX 9708609 A 19960419; ZA 963518 A 19960503