

Title (en)
METHOD AND APPARATUS FOR AMPLIFYING AN ELECTRICAL SIGNAL

Title (de)
VERFAHREN UND GERÄT ZUR VERSTÄRKUNG EINES ELEKTRISCHEN SIGNALS

Title (fr)
PROCEDE ET APPAREIL DESTINES A AMPLIFIER UN SIGNAL ELECTRIQUE

Publication
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Application
EP 96922628 A 19960628

Priority

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Abstract (en)
[origin: WO9702655A1] A dual stage amplifier (50) includes an amplifier stage (52) having an input and an output, a first output stage (58) having an input and an output, a second output stage (54) having an input and an output and an error stage (56) having an input and an output. The first output stage (58) output is coupled with the output of the amplifier (50) and responsive to the amplifier stage (52). If the first output stage (58) output is coupled to an output load (RL4), the first output stage (58) is capable of supplying an output current to the output load (RL4) such that a first output voltage related to an input voltage at the input of the amplifier stage (52) is maintained across the output load (RL4). In turn, the second output stage (54) input is coupled with the output of the amplifier stage (52) and in parallel with the first output stage (58). Additionally, the second output stage (54) output is coupled to the input of the error stage (56) while the output of the error stage (56) is coupled to the input of the amplifier stage (52). The error stage (56) is capable of providing a feedback current to the amplifier stage (52) input such that a difference between a second output voltage generated by the second output stage (54) and a desired output voltage is reduced.

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Citation (search report)

- [XY] US 5117200 A 19920526 - SCOTT III BAKER P L [US]
- [Y] US 4751474 A 19880614 - GOLA ALBERTO [IT]
- [Y] US 5121080 A 19920609 - SCOTT III BAKER P L [US], et al
- [Y] US 4524327 A 19850618 - MASUDA EIJI [JP], et al
- See references of WO 9702655A1

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