

Title (en)

Switching system comprising distributed elements allowing attachment to lines adapters

Title (de)

Vermittlungssystem zur Verbindung von Leitungsadaptern, das verteilte Elemente enthält

Title (fr)

Système de commutation contenant des éléments distribués permettant la connexion d'adapteurs de ligne

Publication

**EP 0849916 B1 20070502 (EN)**

Application

**EP 97480051 A 19970819**

Priority

- EP 97480051 A 19970819
- EP 96480125 A 19961220

Abstract (en)

[origin: EP0849916A2] A switching system comprising a switching structure (1130) for routing cells from a set of M input ports towards a set of M output ports. The systems further includes a set of distributed individual Switch Core Access Layer elements (S.C.A.L.) (1000) which communicating with one input and output port of the switching structure by means of a set of serial communication links (1400, 1600). Each SCAL element provides attachment to at least one Protocol Adapter (Protocol Engine 1600- 1900), and comprises a set of circuits (PINT 511-515; 611-614), each PINT circuit being associated with a corresponding one of said at least Protocol Adapter (Protocol Engine 1600-1900). The receive part of each circuit receives the data cells from the attached Protocol Adapter (Protocol Engine 1600) and includes at least one first FIFO storage (701-704) for storing the cells being received. Additionally, there is introduced at least one extra byte to every cell, which at least one extra byte is reserved for a routing header dedicated for controlling either the routing process within the switching structure. Each transmit part of the destination PINT circuit comprises at least one second FIFO storage (801-802) having a substantially greater capacity than said of said first FIFO storage. Every Transmit part receives all the cells that are generated at the corresponding output port but uses the at least one extrabyte for determining whether or not the cell is to be entered into the at least second FIFO contained in a considered PINT circuit. Additionally, each distributed individual SCAL element comprises control means for performing Time Division Multiplexing (TDM) access of the at least one first FIFO and second FIFO so that the high rate communication between the switching structure and SCALs can be distributed between the different Protocol Adapters. A set of serializer/deserializer permit the use of cheap serialized communication links between the centralized switching system and the different SCAL elements.

<IMAGE>

IPC 8 full level

**H04L 12/56** (2006.01); **H04L 12/933** (2013.01); **H04L 12/935** (2013.01); **H04L 49/111** (2022.01); **H04Q 11/04** (2006.01); **H04L 12/70** (2013.01)

CPC (source: EP)

**H04L 49/1553** (2013.01); **H04L 49/3081** (2013.01); **H04Q 11/0478** (2013.01); **H04L 2012/5642** (2013.01)

Cited by

US6016307A; WO0241663A3; WO0038375A1; US7088710B1; US9806988B2; US7443790B2; US7184444B2; US7145867B2; US6665495B1; US8116315B2; US7526203B2

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