

Title (en)

Memory control in recording and/or reproducing apparatus

Title (de)

Steuerung eines Speichers in einem Aufzeichnungs- und/oder Wiedergabegerät

Title (fr)

Commande de mémoire dans un appareil d'enregistrement et/ou de reproduction

Publication

EP 0853318 A3 20020417 (EN)

Application

EP 98200752 A 19940414

Priority

- EP 94302668 A 19940414
- JP 11930393 A 19930423

Abstract (en)

[origin: EP0621598A2] A memory control device having a memory (13) for reading out and holding managing data from a recording medium (1) is equipped with a memory controller (12) for outputting write-in/read-out addresses through an address generating circuit and receiving write-in/read-out data, and a system controller (11) for controlling the memory controller (12) to execute the data write-in/read-out operation on the memory. When link data in the managing information is supplied from the system controller (11) or obtained from the data read in from the memory (13), an address to be next accessed on the basis of the link data is calculated and the calculated address is output to the memory (13). <IMAGE>

IPC 1-7

G11B 27/10; **G11B 27/034**; **G11B 27/32**; **G11B 27/11**; **G11B 11/105**

IPC 8 full level

G06F 3/06 (2006.01); **G06F 3/08** (2006.01); **G11B 11/105** (2006.01); **G11B 20/10** (2006.01); **G11B 27/036** (2006.01); **G11B 27/10** (2006.01); **G11B 27/11** (2006.01); **G11B 27/19** (2006.01); **G11B 27/22** (2006.01); **G11B 27/32** (2006.01); **G11B 27/034** (2006.01)

CPC (source: EP)

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Citation (search report)

- [PA] EP 0540164 A1 19930505 - SONY CORP [JP]
- [A] EP 0281415 A2 19880907 - SONY CORP [JP]
- [DA] PATENT ABSTRACTS OF JAPAN vol. 017, no. 438 (P - 1591) 12 August 1993 (1993-08-12)

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