

Title (en)

BANDWIDTH REGULATION SYSTEM FOR MULTICHANNEL MEMORY ARRAYS

Title (de)

BANDBREITENREGELUNGSSYSTEM FÜR MEHRKANALSPEICHERANORDNUNGEN

Title (fr)

SYSTEME DE REGULATION DE LARGEUR DE BANDE POUR RESEAUX DE MEMOIRES A CANAUX MULTIPLES

Publication

**EP 0855055 A2 19980729 (EN)**

Application

**EP 97928404 A 19970714**

Priority

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Abstract (en)

[origin: WO9804975A2] A multichannel information system includes a plurality of input/output devices coupled to a plurality of array controllers through a rotating commutator. Each array controller is coupled to and controls a plurality of disk groups operating as memory arrays. Each input/output device communicates data to and from the array controllers in a plurality of data channels. A bandwidth regulation system is operative within each array controller to authorize channel bandwidths for each application run in the system and to prevent transitory bandwidth demands within any channel from interfering with previously authorized channel bandwidths.

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IPC 8 full level

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