

Title (en)

Fault location arrangement in a power system

Title (de)

Vorrichtung zur schnellen Fehlerortung in Starkstromnetzen

Title (fr)

Dispositif de location de faute dans un réseau de courant fort

Publication

EP 0856929 A3 19991006 (DE)

Application

EP 98100789 A 19980119

Priority

DE 19703347 A 19970130

Abstract (en)

[origin: EP0856929A2] The device has measurement transducers and rectifiers followed by signal memories, a signal processing unit and a display unit. D.c. signals applied to the input region are fed to evaluation stages (7-9) which generate signals when rapid current changes occur. Voltage isolation elements (10-12) connected to the output sides of the evaluation stages feed memory stages (13-15) controlled by a process controller (23) and a timing stage (24) to eliminate overcurrent signals so that true fault signals activate a display unit (19-21).

IPC 1-7

H02H 3/44

IPC 8 full level

H02H 3/44 (2006.01); **H02H 3/04** (2006.01)

CPC (source: EP)

H02H 3/44 (2013.01); **H02H 3/042** (2013.01)

Citation (search report)

- [A] WO 9102395 A1 19910221 - SIGMA INSTR DIVISION [US]
- [A] WO 9107795 A1 19910530 - PACIFIC SCIENTIFIC CO [US]

Cited by

CN112162174A

Designated contracting state (EPC)

AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)

EP 0856929 A2 19980805; EP 0856929 A3 19991006; EP 0856929 B1 20030709; AT E244949 T1 20030715; DE 19703347 A1 19980806; DE 59808934 D1 20030814

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EP 98100789 A 19980119; AT 98100789 T 19980119; DE 19703347 A 19970130; DE 59808934 T 19980119