

Title (en)

Semiconductor storage device capable of accurately collectively executing erase verify operation on all memory cells

Title (de)

Halbleiterspeicheranordnung mit der Fähigkeit, genaue und gemeinsame Löschrprüfung von allen Speicherzellen auszuführen

Title (fr)

Dispositif de mémoire à semi-conducteurs capable d'exécuter la vérification d'effacement de toutes les cellules de mémoire avec précision et collectivement

Publication

EP 0862184 A3 19990818 (EN)

Application

EP 98301296 A 19980223

Priority

JP 4537497 A 19970228

Abstract (en)

[origin: EP0862184A2] A verify operation is accurately collectively executed on all memory cells. In verify operation, first, the levels of a pre-charge signal phi pre and a collective erase verify mode selection signal phi aev are made to be "L", so that a common bit line 5 and all bit lines BL0 through BLm are individually charged with a pre-charge voltage Vpre. Thereafter, the level of the collective erase verify mode selection signal phi aev is made to be "H" to connect the common bit line 5 to all the bit lines BL0 through BLm and a sense amplifier 8, and all word lines WL0 through WLn are selected by a row decoder circuit 2. Then, there is watched an event that the common bit line 5 is discharged and an output signal OUT of the sense amplifier 8 becomes "L" due to the existence of a non-erased memory cell in a memory cell array 1. In this case, the discharge of the common bit line 5 occurs when at least one non-erased memory cell transistor MT exists in the memory cell array 1, and therefore, the verify operation can be accurately collectively executed on all the memory cells. <IMAGE>

IPC 1-7

G11C 16/06

IPC 8 full level

G11C 16/02 (2006.01); **G11C 8/10** (2006.01); **G11C 16/04** (2006.01); **G11C 16/06** (2006.01); **G11C 16/34** (2006.01)

CPC (source: EP KR US)

G11C 8/10 (2013.01 - EP US); **G11C 16/00** (2013.01 - KR); **G11C 16/344** (2013.01 - EP US); **G11C 16/3445** (2013.01 - EP US); **G11C 2216/28** (2013.01 - EP US)

Citation (search report)

- [A] WO 9524057 A2 19950908 - ROHM CORP [US]
- [A] EP 0572240 A2 19931201 - TOSHIBA KK [JP], et al
- [A] US 5357463 A 19941018 - KINNEY WAYNE I [US]

Cited by

FR2820545A1; WO02063632A1

Designated contracting state (EPC)

AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)

EP 0862184 A2 19980902; **EP 0862184 A3 19990818**; **EP 0862184 B1 20040114**; DE 69821039 D1 20040219; DE 69821039 T2 20041028; JP 3501916 B2 20040302; JP H10241378 A 19980911; KR 100297669 B1 20011025; KR 19980071781 A 19981026; TW 380256 B 20000121; US 6081452 A 20000627

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EP 98301296 A 19980223; DE 69821039 T 19980223; JP 4537497 A 19970228; KR 19980006314 A 19980227; TW 87102263 A 19980218; US 2599698 A 19980219