

Title (en)

METHOD AND APPARATUS FOR POINT LANDMARK DESIGN OF INTEGRATED CIRCUITS

Title (de)

VERFAHREN UND APPARAT ZUM ENTWURF VON PUNKTFÖRMIGEN MARKIERUNGEN VON INTEGRIERTEN SCHALTUNGEN

Title (fr)

PROCEDE ET APPAREIL DE CONCEPTION DE REPERES DE POINTS POUR DES CIRCUITS INTEGRES

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Application

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Abstract (en)

[origin: WO9642060A1] A method and apparatus for point landmark design of integrated circuits. A computer is programmed to present a graphical interface to and to receive commands from a user in response to the graphical interface. The computer also receives a description of the features of an integrated circuit design and of the design rules pertaining to a given process technology and other design constraints. The computer then associates at least one feature point with each feature in the integrated circuit design and associates a list of attributes with each type of feature used in the integrated circuit design. The computer also associates at least two constraints (ref1, p1) with each feature point and associates a list of constraint attributes with each constraint. Then the computer performs operations on the list of attributes associated with each point landmark to determine the relative geometric placement of the feature points.

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