

Title (en)

Circuit arrangement for generating an internal supply voltage

Title (de)

Schaltungsanordnung zur Erzeugung einer internen Versorgungsspannung

Title (fr)

Circuit générateur d'une tension d'alimentation interne

Publication

EP 0872789 B1 20010314 (DE)

Application

EP 98105983 A 19980401

Priority

DE 19716430 A 19970418

Abstract (en)

[origin: EP0872789A2] An internal supply voltage (Vccint) generation circuit arrangement including a reference voltage generator (RG) which detects a voltage proportional to the external supply voltage (Vccext) and then generates a reference voltage (Vref) in dependence of the amplitude of this voltage, the reference voltage serving to control the internal supply voltage (Vccint) generating device. At least two constant voltage values of the reference voltage (Vref) can be generated from the reference voltage generator (RG), the latter comprising at least two voltage sources (VREF1,VREF2), one of which is designed with a double current-mirror circuit.

IPC 1-7

G05F 1/46

IPC 8 full level

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CPC (source: EP KR US)

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Designated contracting state (EPC)

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DOCDB simple family (publication)

EP 0872789 A2 19981021; **EP 0872789 A3 19990414**; **EP 0872789 B1 20010314**; CN 1197320 A 19981028; DE 19716430 A1 19981119; DE 59800515 D1 20010419; JP H10301649 A 19981113; KR 100468065 B1 20050414; KR 19980081441 A 19981125; TW 371329 B 19991001; US 6194953 B1 20010227

DOCDB simple family (application)

EP 98105983 A 19980401; CN 98106648 A 19980417; DE 19716430 A 19970418; DE 59800515 T 19980401; JP 11995598 A 19980415; KR 19980013540 A 19980416; TW 87105367 A 19980409; US 6331498 A 19980420