

Title (en)  
Device for time synchronisation

Title (de)  
Anordnung zur Zeitsynchronisation

Title (fr)  
Dispositif pour la synchronisation de temps

Publication  
**EP 0877306 A3 20051005 (DE)**

Application  
**EP 98107258 A 19980421**

Priority  

- DE 19718462 A 19970430
- DE 19739996 A 19970911

Abstract (en)  
[origin: EP0877306A2] The time synchronization arrangement comprises a clock, by which it synchronizes at least two installation devices or electronic building installation or office arrangements, time-wise through synchronization signals adjusted individually to the device or arrangements. The time synchronization arrangement is configured for the output of the adjusted synchronization signals for the installation devices or electronic building installation or office arrangements through corresponding parameters. The arrangement comprises preferably a programmable micro-chip, is integrated in a casing, and is adjusted to the correct time by an external signal, preferably a radio signal.

IPC 1-7  
**G04G 7/00; G04G 5/00**

IPC 8 full level  
**G04G 7/00** (2006.01); **G04G 9/00** (2006.01)

CPC (source: EP)  
**G04G 7/00** (2013.01); **G04G 9/0011** (2013.01)

Citation (search report)  

- [A] US 5121390 A 19920609 - FARRELL JOSEPH K [US], et al
- [A] DE 3803525 A1 19890817 - LICENTIA GMBH [DE]
- [A] SCHLOSSMAIER K ET AL: "An ASIC supporting external clock synchronization for distributed real-time systems", REAL-TIME SYSTEMS, 1996., PROCEEDINGS OF THE EIGHTH EUROMICRO WORKSHOP ON L'AQUILA, ITALY 12-14 JUNE 1996, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 12 June 1996 (1996-06-12), pages 277 - 282, XP010200811, ISBN: 0-8186-7496-2
- [A] HILLEN K K ET AL: "BUILD RECONFIGURABLE PERIPHERAL CONTROLLERS", ELECTRONIC DESIGN, HAYDEN PUB. CO., NEW YORK, NY, US, vol. 38, no. 5, 8 March 1990 (1990-03-08), pages 69 - 72,74,76,, XP000102921, ISSN: 0013-4872

Designated contracting state (EPC)  
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