

Title (en)

Fast switch able to withstand high DC voltage and current, realized by assembly of elementary thyristors

Title (de)

Schneller, als Anordnung elementarer Thyristoren realisierter Schalter mit hoher Gleichspannungs- und Gleichstromfestigkeit

Title (fr)

Interrupteur rapide, capable de supporter une tension et un courant directs élevés, obtenu par assemblage de thyristors élémentaires

Publication

**EP 0880228 B1 20021127 (FR)**

Application

**EP 98401201 A 19980519**

Priority

FR 9706176 A 19970521

Abstract (en)

[origin: EP0880228A1] The fast switching rate thyristor circuit has a number of thyristor stages in series (10,12,14,16,18) each of which has thyristors mounted in parallel. The anodes and cathodes of each thyristor in each stage are connected together. The switch formed from the thyristor combination delivers a peak trigger current for each thyristor which is one third above the nominal mean current for the thyristor.

IPC 1-7

**H03K 17/10**; **H03K 17/12**

IPC 8 full level

**H03K 17/10** (2006.01); **H03K 17/12** (2006.01)

CPC (source: EP)

**H03K 17/105** (2013.01); **H03K 17/125** (2013.01)

Cited by

CN104579272A

Designated contracting state (EPC)

CH DE FR GB IT LI

DOCDB simple family (publication)

**EP 0880228 A1 19981125**; **EP 0880228 B1 20021127**; DE 69809632 D1 20030109; DE 69809632 T2 20030814; FR 2763764 A1 19981127; FR 2763764 B1 19991112

DOCDB simple family (application)

**EP 98401201 A 19980519**; DE 69809632 T 19980519; FR 9706176 A 19970521