

Title (en)

Circuit arrangement for generating a load-independent DC voltage

Title (de)

Schaltungsanordnung zur Erzeugung einer lastunabhängigen Gleichspannung

Title (fr)

Circuit pour générer une tension continue indépendante de la charge

Publication

EP 0886200 B1 20020502 (DE)

Application

EP 98110544 A 19980609

Priority

DE 19725842 A 19970618

Abstract (en)

[origin: EP0886200A2] The circuit arrangement includes a first rectifier arrangement (GL1) with an AC voltage connection (EK1, EK2) and two output terminals (AK1, AK2), and a current regulator arrangement (SRA) connected to the output terminals of the first rectifier arrangement. A second rectifier arrangement is connected to two output terminals (AK3, AK4) of the current regulator arrangement, which provides an output voltage (Ua) at two output terminals (AK5, AK6). A voltage measurement arrangement (MA) is connected to the output terminals of the second rectifier arrangement to provide a voltage signal (SS) at its output. A feedback branch includes a regulator arrangement (RA) with an integrator arrangement (IN) for feeding the voltage signal to an input terminal (EK) of the current regulation arrangement. A function generator is arranged in the feedback branch in connection to the regulator arrangement. The function generator provides an output signal (y) from an input signal (x), according to a predetermined function ($y=f(x)$), whereby the functions increases at least partially with increasing input signal.

IPC 1-7

G05F 1/613; H02M 1/00

IPC 8 full level

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CPC (source: EP US)

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Cited by

CN103516191A; DE10355670A1; DE10355670B4; US7031173B2; US6946867B2

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DOCDB simple family (publication)

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