Title (en)
CACHE MULTI-BLOCK TOUCH MECHANISM FOR OBJECT ORIENTED COMPUTER SYSTEM

Title (de)
CACHE-MEHRFACHBLOCKKONTAKTMECHANISMUS FÜR OBJEKTORIENTIERTES RECHNERSYSTEM

Title (fr)
MECANISME DE CONTACTS MULTIBLOCS D'ANTEMEMOIRE POUR LES SYSTEMES ORIENTES OBJET

Publication
EP 0890148 A1 19990113 (EN)

Application
EP 96944764 A 19961205

Priority
• US 9619469 W 19961205
• US 62326696 A 19960328

Abstract (en)
[origin: WO9736234A1] An object-oriented computer apparatus (100) generates a first instruction stream (124) by using a multi-block cache touch instruction generator (122) to insert a multi-block cache touch instruction (300) into a second instruction stream (123). The execution of the multi-block cache touch instruction by the processing unit (110) causes a prefetch of at least one of a plurality of multiple blocks of data or code from a main memory (120) into a plurality of cache lines of a cache memory. These multi-block touch instructions indicate the beginning address of a desired block in computer system memory and the amount of the block to be prefetched into cache. In response the cache will prefetch multiple lines into the cache.

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G06F 12/08

IPC 8 full level
G06F 9/38 (2006.01); **G06F 9/45** (2006.01); **G06F 12/08** (2006.01); **G06F 12/0862** (2016.01)

CPC (source: EP KR)
G06F 8/4442 (2013.01 - EP); **G06F 9/30047** (2013.01 - EP); **G06F 9/3802** (2013.01 - EP); **G06F 9/383** (2013.01 - EP); **G06F 12/08** (2013.01 - KR); **G06F 12/0862** (2013.01 - EP); G06F 2212/6028 (2013.01 - EP)

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