

Title (en)
Inrush current limiting circuit

Title (de)
Einschaltstrombegrenzungsschaltung

Title (fr)
Circuit limiteur de courant d'appel

Publication
EP 0895443 B1 20020925 (FR)

Application
EP 98410084 A 19980728

Priority
FR 9710034 A 19970731

Abstract (en)
[origin: EP0895443A1] The lamp (L) is series-connected, across the AC e.g. mains supply (P,N), with a single-pole switch and a peak-logging circuit based on two MOSFET transistors (M1,M2). The load-carrying current connection between MOSFETs includes a resistance (Rm) providing current-related input levels to bipolar transistors (T1,T2) controlling the MOSFETs. Bypass diodes (D1,D2) ensure one MOSFET is inoperative during positive current half-cycles. The other during negative half-cycles. The series resistor provides current measurement for the relevant control transistor in both half-cycles. In one variant, a single peak-lopper is used, with a full-wave rectifier. In another, a MOSFET is used in a chopping mode. Optionally, IGBTs replace MOSFETs.

IPC 1-7
H05B 39/02

IPC 8 full level
H05B 39/02 (2006.01)

CPC (source: EP US)
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Cited by
EP3297112A1; FR3056346A1; US10474172B2

Designated contracting state (EPC)
DE FR GB IT

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EP 0895443 A1 19990203; EP 0895443 B1 20020925; DE 69808202 D1 20021031; FR 2767017 A1 19990205; FR 2767017 B1 19991015; US 6445144 B1 20020903

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