

Title (en)

Shallow trench isolation for DRAM trench capacitor

Title (de)

Flachgraben-Isolation für DRAM Grabenkondensator

Title (fr)

Fossé d'isolation peu profond pour capacité ensillonnée de type DRAM

Publication

EP 0908948 A2 19990414 (EN)

Application

EP 98110953 A 19980616

Priority

US 94391097 A 19970930

Abstract (en)

A random access memory cell having a trench capacitor formed below the surface of the substrate. A shallow trench isolation is provided to isolate the memory cell from other memory cells of a memory array. The shallow trench isolation includes a top surface raised above the substrate to reduce oxidation stress. <IMAGE>

IPC 1-7

H01L 21/8242; **H01L 27/108**

IPC 8 full level

H01L 21/8242 (2006.01); **H01L 27/108** (2006.01)

CPC (source: EP KR US)

H10B 12/00 (2023.02 - KR); **H10B 12/038** (2023.02 - EP US); **H10B 12/37** (2023.02 - EP US)

Cited by

DE10205077A1; DE10205077B4; US7256440B2; WO03067596A3

Designated contracting state (EPC)

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DOCDB simple family (publication)

US 5867420 A 19990202; CN 1213857 A 19990414; EP 0908948 A2 19990414; EP 0908948 A3 20011024; JP H11163301 A 19990618; KR 19990030047 A 19990426; TW 521399 B 20030221

DOCDB simple family (application)

US 94391097 A 19970930; CN 98120705 A 19980923; EP 98110953 A 19980616; JP 27729298 A 19980930; KR 19980039358 A 19980923; TW 91101864 A 19980916