

Title (en)
HIGH SPEED PROGRAMMABLE LOGIC ARCHITECTURE

Title (de)
PROGRAMMBIERBARE HOCHGESCHWINDIGKEITS-ARCHITEKTUR

Title (fr)
ARCHITECTURE DE LOGIQUE PROGRAMMABLE GRANDE VITESSE

Publication
EP 0913032 A1 19990506 (EN)

Application
EP 96912515 A 19960329

Priority
US 9604409 W 19960329

IPC 1-7
H03K 19/177

IPC 8 full level
H03K 19/177 (2006.01)

Designated contracting state (EPC)
AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)
EP 0913032 A1 19990506; EP 0913032 A4 20010314; JP 2000507774 A 20000620; JP 3774234 B2 20060510

DOCDB simple family (application)
EP 96912515 A 19960329; JP 53522297 A 19960329