

Title (en)
CIRCUIT ARRANGEMENT FOR REDUCING TRANSIENTS CAUSED BY AN ELECTROMECHANICAL SWITCH WITH OVERCURRENT PROTECTION

Title (de)
SCHALTUNGSANORDNUNG ZUR VERRINGERUNG DER TRANSIENTEN, DIE VON EINEM ELEKTROMECHANISCHEN SCHALTER MIT ÜBERTSTROMSCHUTZ VERURSACHT WERDEN

Title (fr)
AMENAGEMENT D'UN CIRCUIT POUR LA REDUCTION DES COURANTS TRANSITOIRES CAUSES PAR UN COMMUTATEUR ELECTROMECHANIQUE AU MOYEN D'UNE PROTECTION A SURINTENSITE

Publication
EP 0914664 A1 19990512 (EN)

Application
EP 97929330 A 19970703

Priority
• FI 9700433 W 19970703
• FI 962973 A 19960725

Abstract (en)
[origin: WO9805050A1] When voltage is connected to load by using an electromechanical switch, high switching transients are generated. They can be suppressed significantly by placing a controllable semiconductor switch (21) in parallel with the switch (4), in which case the total load current (ITOT) is distributed among both switches. The first comparison circuit (25) monitors the current (Iswitch) which passes through the electromechanical switch and compares it to a very small reference value. As soon as the current which passes through the switch reaches the reference value, the first comparison circuit gives a control voltage which forces the semiconductor switch into the conductive state. Because of this, the rate of increase of the current which passes through the switch (4) decreases essentially because a large part of the total load current (ITOT) passes through the semiconductor switch (21). The second comparison circuit monitors the total load current (ITOT) and compares it to a maximum current value at which the load must be disconnected from the voltage source. When the total load current exceeds the reference value, the second comparison circuit gives a control voltage which immediately forces the semiconductor switch to the nonconductive state. At this point the current which passes through the electromechanical switch increases suddenly so that the internal overcurrent protection of the switch reacts immediately and opens the switch.

IPC 1-7
H01H 9/54; H02H 3/08

IPC 8 full level
H01H 9/54 (2006.01); **H02H 3/00** (2006.01); **H02H 3/087** (2006.01)

CPC (source: EP)
H01H 9/542 (2013.01); **H02H 3/006** (2013.01); **H02H 3/087** (2013.01)

Citation (search report)
See references of WO 9805050A1

Designated contracting state (EPC)
BE CH DE DK FR GB IT LI NL PT

DOCDB simple family (publication)
WO 9805050 A1 19980205; AU 3346897 A 19980220; CN 1064476 C 20010411; CN 1230284 A 19990929; EP 0914664 A1 19990512; FI 107848 B 20011015; FI 962973 A0 19960725; FI 962973 A 19980126

DOCDB simple family (application)
FI 9700433 W 19970703; AU 3346897 A 19970703; CN 97197925 A 19970703; EP 97929330 A 19970703; FI 962973 A 19960725