Title (en)
Pulse generator

Title (de)
Impulsgenerator

Title (fr)
Générateur d'impulsions

Publication
**EP 0917290 A2 19990519 (EN)**

Application
**EP 98123372 A 19941223**

Priority
• EP 94120564 A 19941223
• JP 32766993 A 19931224

Abstract (en)
A pulse phase difference encoding apparatus comprises a delay circuit (52) which is constituted by a plurality of interconnected delay elements, to which an input signal (PA) is input and which sequentially outputs, from a plurality of connecting points of the delay elements, a plurality of delayed signals obtained by delaying the input signal by a delay period determined by the numbers of the delay elements. Several pulse phase difference encoding circuits (52, 54) to each of which a pulse signal (PB, PX) is input, and each of which detects a delayed signal corresponding to an input timing of the pulse signal (PB, PX) among the plurality of delayed signals output by the delay circuit (52), outputs position data representing a position of the delay element of the delay circuit which issues the delayed signal, and generates digital data (D0-D14) corresponding to a phase difference between the input signal (PA) and the pulse signal (PB, PX) by using the position data. The several pulse phase difference encoding circuits (54, 56) share the delay circuit (52) in generating the digital data. <IMAGE>

IPC 1-7
**H03K 5/26**; G01R 25/08

IPC 8 full level
**G01R 25/00** (2006.01); **G01R 25/08** (2006.01); **G01R 29/02** (2006.01); **H03K 3/03** (2006.01); **H03K 3/354** (2006.01); **H03K 5/00** (2006.01); **H03K 5/135** (2006.01); **H03K 5/26** (2006.01); **H03L 7/06** (2006.01); **H03L 7/085** (2006.01)

CPC (source: EP US)
**H03K 3/0315** (2013.01 - EP US); **H03K 5/135** (2013.01 - EP US)

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EP1347575A1; EP1118943A1; FR2803925A1; US7123071B2; US6771726B2

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
**EP 0660518 A1 19950628**; **EP 0660518 B1 19990609**; DE 69418987 D1 19990715; DE 69418987 T2 20000316; DE 69430334 D1 20020508; DE 69430334 T2 20021114; EP 0917290 A2 19990519; EP 0917290 A3 19990526; EP 0917290 B1 20020403; JP 2900772 B2 19990602; JP H07183800 A 19950721; US 5477196 A 19951219

DOCDB simple family (application)
**EP 94120564 A 19941223**; DE 69418987 T 19941223; DE 69430334 T 19941223; EP 98123372 A 19941223; JP 32766993 A 19931224; US 36264894 A 19941223