

Title (en)
SYSTEM FOR PROTECTING SEMICONDUCTOR CHIPS AGAINST ANALYSIS

Title (de)
ANALYSIERSCHUTZ FÜR EINEN HALBLEITERCHIP

Title (fr)
SYSTEME DE PROTECTION D'ANALYSE POUR PUCE A SEMI-CONDUCTEUR

Publication
EP 0931344 A1 19990728 (DE)

Application
EP 97943756 A 19970911

Priority
• DE 9702039 W 19970911
• DE 19639033 A 19960923

Abstract (en)
[origin: DE19639033C1] The invention concerns a system for protecting a semiconductor chip (1) with a covering (4) against copying, the system comprising an oscillating circuit which is capable of resonance and whose elements are located on the chip surface (3) and inside the covering (4), and an evaluation circuit connected thereto. The circuit deactivates the semiconductor chip when the oscillating circuit is brought out of resonance. This off-resonance adjustment of the oscillating circuit occurs when the covering (4), which acts as a dielectric of the capacitor, is removed. It is impossible to operate the semiconductor chip (1) for copying or analysis purposes once its covering (4) has been removed.

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H01L 23/58

IPC 8 full level
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CPC (source: EP US)
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C-Set (source: EP US)
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