

Title (en)

METHOD FOR MAKING A CONDUCTOR, OR ELECTRIC CIRCUIT BALANCED IN RADIOPHYSICAL INTERFERENCE SUCH AS MICRO-DISCHARGE AND CORRESPONDING CONDUCTOR OR CIRCUIT

Title (de)

HERSTELLUNGSVERFAHREN VON EINEM LEITER ODER EINER SCHALTUNG MIT EINER FUNK-ENTSTÖRUNGSKOMPENSIERUNG (ESD) SOWIE DANACH HERGESTELLTE LEITER ODER SCHALTUNG

Title (fr)

PROCEDE DE FABRICATION D'UN CONDUCTEUR, OU CIRCUIT, ELECTRIQUE COMPENSE EN PARASITES RADIOPHYSIQUES TELS QUE MICRO-DECHARGES ET CONDUCTEUR OU CIRCUIT CORRESPONDANT

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Application

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Abstract (en)

[origin: FR2754630A1] The invention concerns a method for making a conductor, or electric circuit balanced in radiophysical interference, including those derived from the circuit itself. It consists in applying a semiconducting material (2) of per unit length resistivity for maintaining the external surface of the conductor (1) to the electric circuit with a static electric potential close to that of the conductor (1) and for absorbing the erratic discharge currents in order to eliminate the interface micro-discharge interference phenomena. The invention is applicable to the fields of high fidelity equipment, house automation, and instrumentation.

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H05K 9/00; H01F 1/44

IPC 8 full level

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