

Title (en)

METHOD AND CIRCUIT FOR DIGITAL MODULATION AND METHOD AND CIRCUIT FOR DIGITAL DEMODULATION

Title (de)

VERFAHREN UND SCHALTUNG ZUR DIGITALEN MODULATION SOWIE DEMODULATIONSSCHALTUNG

Title (fr)

PROCEDE ET CIRCUIT DE MODULATION NUMERIQUE ET PROCEDE ET CIRCUIT DE DEMODULATION NUMERIQUE

Publication

EP 0932258 A4 19991208 (EN)

Application

EP 97943166 A 19971009

Priority

- JP 9703632 W 19971009
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- JP 31430696 A 19961110

Abstract (en)

[origin: EP0932258A1] A digital modulation methods is provided in which at a head of an input block (pre-translation), each of a plurality of different types of initial data of t bits is multiplexed to generate a plurality of different types of multiplexed blocks, t bits at the head and immediately following t bits of each of the multiplexed blocks are subjected to exclusive OR operation, the immediately following t bits are replaced by the result of operation, the replaced t bits and the immediately following t bits are subjected to an exclusive OR operation, and the immediately following t bits are replaced by the result of operation, and thereafter in the similar manner, convolution operation is executed, whereby a plurality of different types of translated blocks are produced by the convolution operation, and DC components of thus provided translated blocks are calculated respectively, absolute values of the respective DC components are compared with each other, and a translated block having the minimum value is selected and output externally. <IMAGE>

IPC 1-7

H03M 7/14

IPC 8 full level

H03M 7/14 (2006.01); **H03M 5/14** (2006.01); **H03M 13/31** (2006.01)

CPC (source: EP KR US)

H03M 5/145 (2013.01 - EP US); **H03M 7/14** (2013.01 - KR); **H03M 13/31** (2013.01 - EP US)

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