

Title (en)

Method and apparatus for processing program loops

Title (de)

Verfahren und Vorrichtung zur Verarbeitung von Programmschleifen

Title (fr)

Procédé et dispositif de traitement de boucles de programme

Publication

EP 0933703 A2 19990804 (EN)

Application

EP 99300596 A 19990127

Priority

US 7290598 P 19980128

Abstract (en)

A method of operating a multiple execution unit microprocessor in a software pipelined loop is disclosed. This method executes the loop body before the pipeline is fully initialized, thus replacing prolog instructions with additional loop iterations. The method has the potential to greatly reduce prolog size for many software pipelined loops. As a further aspect of the method, the loop results are insulated from any deleterious effects of loop body execution prior to full initialization--methods for accomplishing this are disclosed, including array overallocation, conditional execution of some loop body instructions, and register initialization.

IPC 1-7

G06F 9/45; **G06F 9/38**

IPC 8 full level

G06F 9/32 (2006.01); **G06F 9/38** (2006.01)

CPC (source: EP US)

G06F 9/325 (2013.01 - EP US); **G06F 9/3836** (2013.01 - EP US); **G06F 9/3842** (2013.01 - EP US)

Cited by

EP1160663A3

Designated contracting state (EPC)

DE FR GB IT NL

DOCDB simple family (publication)

EP 0933703 A2 19990804; **EP 0933703 A3 20010829**; **EP 0933703 B1 20070829**; DE 69936952 D1 20071011; DE 69936952 T2 20080521; US 6289443 B1 20010911

DOCDB simple family (application)

EP 99300596 A 19990127; DE 69936952 T 19990127; US 23825999 A 19990127