

Title (en)

Method and apparatus for processing program loops

Title (de)

Verfahren und Vorrichtung zur Verarbeitung von Programmschleifen

Title (fr)

Procédé et dispositif de traitement de boucles de programme

Publication

EP 0933703 A2 19990804 (EN)

Application

EP 99300596 A 19990127

Priority

US 7290598 P 19980128

Abstract (en)

A method of operating a multiple execution unit microprocessor in a software pipelined loop is disclosed. This method executes the loop body before the pipeline is fully initialized, thus replacing prolog instructions with additional loop iterations. The method has the potential to greatly reduce prolog size for many software pipelined loops. As a further aspect of the method, the loop results are insulated from any deleterious effects of loop body execution prior to full initialization-methods for accomplishing this are disclosed, including array overallocation, conditional execution of some loop body instructions, and register initialization.

IPC 1-7

G06F 9/45; G06F 9/38

IPC 8 full level

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CPC (source: EP US)

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