

Publication

EP 0935197 A3 19990922

Application

EP 99650010 A 19990205

Priority

US 1978998 A 19980206

Abstract (en)

[origin: EP0935197A2] A data processor is provided in which an embedded emulator communicates with a control emulation system using a serial communications link involving one pin (EA) of the data processor package. <IMAGE> <IMAGE>

IPC 1-7

G06F 11/00

IPC 8 full level

G06F 11/26 (2006.01); **G06F 11/36** (2006.01)

CPC (source: EP US)

G06F 11/261 (2013.01 - EP US); **G06F 11/3656** (2013.01 - EP US)

Citation (search report)

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DOCDB simple family (publication)

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