

Title (en)

Thermistor manufacturing process and application of thermistor as current limiter

Title (de)

Verfahren zur Herstellung einer Kaltleiteranordnung sowie Verwendung der Kaltleiteranordnung als Strombegrenzer

Title (fr)

Méthode de fabrication d'une thermistance et son application comme dispositif de limitation de courant

Publication

**EP 0939410 A3 20000726 (DE)**

Application

**EP 99810105 A 19990209**

Priority

DE 19808025 A 19980226

Abstract (en)

[origin: EP0939410A2] A cold conductor structure is produced by fixing an insulating ceramic plate (1) to each side of a positive temperature coefficient of resistance (PTC) metal layer (2) with a solder or adhesive layer (3). Independent claims are also included for the following: (i) a cold conductor structure produced as described above, the ratio of the total cross-sectional area of the solder or adhesive layers to the cross-sectional area of the metal layer being less than 4/5, especially 0.12; and (ii) a current limiter consisting solely of the above cold conductor structure.

IPC 1-7

**H01C 7/02**

IPC 8 full level

**H01C 1/084** (2006.01); **H01C 1/14** (2006.01); **H01C 7/02** (2006.01)

CPC (source: EP)

**H01C 1/084** (2013.01); **H01C 1/1406** (2013.01); **H01C 7/02** (2013.01)

Citation (search report)

- [YDA] EP 0713228 A1 19960522 - ABB MANAGEMENT AG [CH]
- [Y] DE 4343149 A1 19950622 - ABB PATENT GMBH [DE]
- [A] DE 2510322 A1 19760819 - BBC BROWN BOVERI & CIE
- [A] DE 4130772 A1 19930429 - SIEMENS MATSUSHITA COMPONENTS [DE]
- [A] EP 0773562 A2 19970514 - ABB RESEARCH LTD [CH]
- [A] US 4901051 A 19900213 - MURATA TSUNEO [JP], et al

Cited by

CN103680779A; TWI461386B

Designated contracting state (EPC)

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)

**EP 0939410 A2 19990901**; **EP 0939410 A3 20000726**; DE 19808025 A1 19990902

DOCDB simple family (application)

**EP 99810105 A 19990209**; DE 19808025 A 19980226