

Title (en)

MIS TRANSISTOR WITH SELF-ALIGNED METAL GRID AND METHOD FOR MAKING IT

Title (de)

MIS-TRANSISTOR MIT EINEM SELBSTJUSTIERTEN METALLISCHEN GATTER UND VERFAHREN ZUR DEREN HERSTELLUNG

Title (fr)

TRANSISTOR MIS A GRILLE METALLIQUE AUTO-ALIGNEE ET SON PROCEDE DE FABRICATION

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Application

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Abstract (en)

[origin: FR2757312A1] The invention concerns a MIS transistor with self-aligned metal grid and the method for making it. The method consists in the following steps: a) producing on a substrate (100) a dummy grid in a material capable of resisting to a thermal treatment; b) forming in the substrate self-aligned source and drain regions (118, 120) on the dummy grid; c) laterally coating the dummy grid with an electrically insulating material (124, 126); d) eliminating the dummy grid and forming in its place a permanent grid (136) in a material with weak resistivity. The invention is useful for making ultrahigh frequency circuits.

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IPC 8 full level

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