

Title (en)

SEMICONDUCTOR STORAGE ARRANGEMENT WITH STACKED CAPACITOR

Title (de)

HALBLEITERSPEICHERANORDNUNG MIT STAPELKONDENSATOR

Title (fr)

SYSTEME DE MEMOIRE A SEMI-CONDUCTEUR A CONDENSATEUR EMPILE

Publication

EP 0946984 A1 19991006 (DE)

Application

EP 97942795 A 19970905

Priority

- DE 9701964 W 19970905
- DE 19639899 A 19960927

Abstract (en)

[origin: DE19639899A1] The invention concerns a storage arrangement (1) which consists of identical storage cells and comprises storage capacitors which are disposed above the selection transistors and whose first electrodes (14) are strip-shaped and disposed perpendicularly on a first main surface (2). The surfaces of the first electrodes (14) and hence the capacitor surfaces can be varied, for example, by varying the heights of the first electrodes (14) or, when the cell surfaces (5) are sensibly arranged, by overlapping cell surfaces (5) adjacent the first electrodes (14).

IPC 1-7

H01L 27/108; H01L 21/8242

IPC 8 full level

H10B 12/00 (2023.01)

CPC (source: EP KR)

H10B 12/00 (2023.02 - KR); **H10B 12/033** (2023.02 - EP); **H10B 12/318** (2023.02 - EP)

Designated contracting state (EPC)

DE FR GB IE IT

DOCDB simple family (publication)

DE 19639899 A1 19980402; DE 19639899 B4 20050707; CN 1231766 A 19991013; EP 0946984 A1 19991006; JP 2001511306 A 20010807;
KR 20000048659 A 20000725; TW 373321 B 19991101; WO 9813877 A1 19980402

DOCDB simple family (application)

DE 19639899 A 19960927; CN 97198331 A 19970905; DE 9701964 W 19970905; EP 97942795 A 19970905; JP 51513198 A 19970905;
KR 19997002603 A 19990326; TW 86113012 A 19970909