

Title (en)  
Voltage reference circuit

Title (de)  
Referenzspannungsschaltung

Title (fr)  
Circuit de tension de référence

Publication  
**EP 0952509 B1 20070530 (DE)**

Application  
**EP 99105492 A 19990317**

Priority  
DE 19817791 A 19980421

Abstract (en)  
[origin: EP0952509A2] A reference voltage circuit based on a bipolar transistor circuit (T1,T2a,T2b) for supplying a reference voltage (Uref) derived from a summed voltage of first and second voltages, also includes adjustment or compensation devices (Z1-Z3,M1-M10) for adjusting the reference voltage (Uref). The adjustment/compensation devices (Z1-Z3,M1-M10) are arranged and designed so that they adjust the collector current (IC1,IC2a) of at least one bipolar transistor (T1,T2a) in the bipolar transistor circuit during activation of the pn-junctions of the transistors (T1,T2a) by forward voltages.

IPC 8 full level  
**G05F 3/30** (2006.01); **G05F 3/26** (2006.01)

CPC (source: EP US)  
**G05F 3/30** (2013.01 - EP US); **G05F 3/267** (2013.01 - EP US)

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