

Title (en)

RECEPTION OF VARIABLE AND RUN-LENGTH ENCODED DATA

Title (de)

EMPFANG VON LAUFLÄNGENKODIERTEN DATEN VARIABLER LÄNGEN

Title (fr)

RECEPTION DE DONNEES DE LONGUEUR VARIABLE ET CODEES PAR LONGUEUR DE LIGNE

Publication

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Application

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Abstract (en)

[origin: WO9935749A2] In a receiver, a variable-length decoder (VLD) derives run-value pairs (RVP) from variable and run-length encoded data (ED) such as, for example, MPEG-encoded data. A run-value pair (RVP) comprises a coefficient value (CV) and a run length (RL). The run length (RL) indicates a number (N) of zero coefficients (0) which precede the coefficient value (CV), N being an integer. A processing circuit (PRC) processes the run-value pairs (RVP) to obtain a decoded data stream (DD). The processing circuit (PRC) comprises a clock circuit (CLC) and a control circuit (CON). The clock circuit (CLC) generates clock cycles (CC) which are synchronous with the decoded data stream (DD). With each run-value pair (RVP) having a run length (RL) unequal to zero, the control circuit (CON) stalls the variable-length decoder (VLD) a number (N) of clock cycles (CC). The number (N) of clock cycles is proportional to the number (N) of zero coefficients indicated by the run length (RL). In such a receiver, the processing circuit (PRC) requires relatively little buffer memory, thus allowing cost-efficient implementations.

IPC 1-7

**H03M 7/46**

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