

Title (en)  
CHIP CARD

Title (de)  
CHIPKARTE

Title (fr)  
CARTE A PUCE

Publication  
**EP 0976099 A2 20000202 (DE)**

Application  
**EP 97920521 A 19970317**

Priority  
• DE 9700538 W 19970317  
• DE 19610098 A 19960315  
• DE 19610507 A 19960318

Abstract (en)  
[origin: CA2248788A1] The disclosure relates to a chip card for contact or contact-free access to a chip inside a chip module. The chip module is arranged in a recess (59) in a card body (49) in such a way that outer contact surfaces (51) of the chip module are disposed on the surface (60) of the card body (49) and inner contact surfaces (53) of the chip module are connected to the wire ends (55, 56) of a coil (57) fitted in the card body to form a transponder unit. The coil is a wire-wound coil (57) and the recess (59) for the chip module is of a depth (t) such that wire ends (55, 56) inside the recess (59) are provided with a flattened contact area (63) by the process which forms the recess (59).

IPC 1-7  
**G06K 19/00**

IPC 8 full level  
**G06K 19/07** (2006.01); **G06K 19/077** (2006.01)

CPC (source: EP US)  
**G06K 19/07743** (2013.01 - EP US); **G06K 19/07749** (2013.01 - EP US); **G06K 19/0775** (2013.01 - EP US); **G06K 19/07769** (2013.01 - EP US); **G06K 19/07779** (2013.01 - EP US)

Citation (search report)  
See references of WO 9735273A2

Designated contracting state (EPC)  
AT BE CH DE ES FR GB IE IT LI NL

DOCDB simple family (publication)  
**DE 19654902 A1 19971023**; **DE 19654902 C2 20000203**; AU 4000097 A 19971010; AU 723495 B2 20000831; CA 2248788 A1 19970925; EP 0976099 A2 20000202; JP 2000506653 A 20000530; JP 3781196 B2 20060531; US 6142381 A 20001107; WO 9735273 A2 19970925; WO 9735273 A3 19980212

DOCDB simple family (application)  
**DE 19654902 A 19960318**; AU 4000097 A 19970317; CA 2248788 A 19970317; DE 9700538 W 19970317; EP 97920521 A 19970317; JP 53303897 A 19970317; US 15500298 A 19980914