

Title (en)  
MATRIX DISPLAY ADDRESSING DEVICE

Title (de)  
MATRIXANZEIGEADRESSIERUNGSVERFAHREN

Title (fr)  
DISPOSITIF D'ADRESSAGE D'UN ECRAN MATRICIEL

Publication  
**EP 0976122 A1 20000202 (FR)**

Application  
**EP 96942417 A 19961218**

Priority  
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• FR 9515405 A 19951222

Abstract (en)  
[origin: WO9723861A1] A device for addressing a matrix display such as an LCD or plasma display. The addressing device comprises a storage stage (70, 198) for receiving, via a demultiplexing stage (220), a plurality of digital data sequences representing previously digitised video luminance signals, and outputting the video luminance signals to a multiplexing stage (230) designed to select a digital data sequence that corresponds to a given combination of subpixels from the plurality of digital data sequences previously stored in said storage stage (70, 198).

IPC 1-7  
**G09G 3/20**

IPC 8 full level  
**G09G 3/20** (2006.01); **G09G 3/36** (2006.01); **G09G 3/28** (2013.01); **G09G 5/393** (2006.01); **G09G 5/399** (2006.01)

CPC (source: EP KR US)  
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Citation (search report)  
See references of WO 9723861A1

Designated contracting state (EPC)  
DE FR GB

DOCDB simple family (publication)  
**WO 9723861 A1 19970703**; DE 69637857 D1 20090416; EP 0976122 A1 20000202; EP 0976122 B1 20090304; FR 2742910 A1 19970627; FR 2742910 B1 19980417; JP 2000502813 A 20000307; JP 4105228 B2 20080625; KR 100425248 B1 20040727; KR 19990071791 A 19990927; US 6252613 B1 20010626

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