

Title (en)  
MEMORY DEVICE AND METHOD

Title (de)  
SPEICHERANORDNUNG UND -VERFAHREN

Title (fr)  
DISPOSITIF ET PROCEDE DE MEMOIRE

Publication  
**EP 0976133 A1 20000202 (EN)**

Application  
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Priority  
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Abstract (en)  
[origin: WO9941751A1] A memory device for storing a sequential image data in succession and outputting the stored image data is provided. The memory device comprises a memory unit comprising N memory blocks, each memory block being capable of individual serving, a write address generator for generating a write address signal to write into the memory unit and a read address generator for generating a read address signal to read from the memory unit. The memory unit further comprises a controller for controlling the write address signal and the read address signal so that each start address for writing and reading for each image data is shifted as unit of the memory block and the writing and reading operations are not simultaneously performed to same memory block, each image data having a size being equivalent to one of M blocks ( $M < N$ ).

IPC 1-7  
**G11C 8/04**

IPC 8 full level  
**G11C 11/401** (2006.01); **G06T 1/60** (2006.01); **G11C 7/10** (2006.01); **G11C 8/00** (2006.01); **G11C 8/04** (2006.01); **H04N 5/44** (2006.01); **H04N 5/907** (2006.01); **H04N 5/21** (2006.01)

CPC (source: EP KR US)  
**G11C 7/1018** (2013.01 - EP US); **G11C 7/1075** (2013.01 - EP US); **G11C 8/04** (2013.01 - EP KR US); **H04N 5/907** (2013.01 - EP US); **H04N 7/012** (2013.01 - EP US); **H04N 5/21** (2013.01 - EP US)

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