

Title (en)

MPEG-4 SYSTEMS COMPLIANT ARCHITECTURE

Title (de)

MPEG-4-SYSTEMKOMPATIBLE ARCHITEKTUR

Title (fr)

ARCHITECTURE COMPATIBLE SYSTEMES MPEG-4

Publication

EP 0978199 A2 20000209 (EN)

Application

EP 99900029 A 19990114

Priority

- EP 99900029 A 19990114
- EP 98400160 A 19980127
- IB 9900038 W 19990114

Abstract (en)

[origin: WO9938329A2] The invention relates to an MPEG-4 Systems compliant architecture subdivided in key modules having a specific task managed by a multi-threading operating system. The MPEG-4 demultiplexer is partitioned into three levels of process. Each process is independent of each other, but synchronization primitives are however provided by the operating system, in the form of semaphores controlling the buffers so that their sizes are kept as small as possible. Application: all kinds of industrial MPEG-4 equipment (desk-tops, set-tops boxes).

IPC 1-7

H04N 7/26

IPC 8 full level

H04N 19/423 (2014.01)

CPC (source: EP KR)

H04N 19/42 (2014.11 - EP); **H04N 19/423** (2014.11 - KR); **H04N 19/436** (2014.11 - EP); **H04N 19/61** (2014.11 - EP)

Citation (search report)

See references of WO 9938329A2

Cited by

US6606329B1

Designated contracting state (EPC)

DE ES FR GB IT SE

DOCDB simple family (publication)

WO 9938329 A2 19990729; WO 9938329 A3 19990930; CN 1150768 C 20040519; CN 1256047 A 20000607; EP 0978199 A2 20000209; JP 2001518272 A 20011009; KR 100639894 B1 20061031; KR 20010005738 A 20010115

DOCDB simple family (application)

IB 9900038 W 19990114; CN 99800066 A 19990114; EP 99900029 A 19990114; JP 53808799 A 19990114; KR 19997008809 A 19990927