

Title (en)

DEVICE AND METHOD FOR CLOCK RECOVERY AND REGENERATION OF DATA

Title (de)

VORRICHTUNG UND VERFAHREN ZUR TAKTRÜCKGEWINNUNG UND DATENREGENERATION

Title (fr)

DISPOSITIF ET PROCEDE D'EXTRACTION DE SIGNAL D'HORLOGE ET DE RECONSTITUTION DE DONNEES

Publication

EP 0985303 A1 20000315 (DE)

Application

EP 98933436 A 19980505

Priority

- DE 9801226 W 19980505
- DE 19722302 A 19970528

Abstract (en)

[origin: DE19722302A1] The invention relates to a device for clock recovery and regeneration of data of HDB3 coded data signals, comprising a device (3) for dividing the data signal (DS) into two channels (DS1,DS2), a threshold value decision circuit device (7) to which the data signals are supplied on both channels and which generates data signals with two logical states (low/high), a scanning device (11) for scanning signals from the threshold value decision circuit device (7) with a system clock (ST) and a decoder device (15) to decode the coded data signal. The invention is characterized in that a timing device (13) is provided to generate an enable-signal (ES) when the signal edge rises and generates an enable-signal (ES) when no signal edge is present after three predeterminable clock counts, in addition to linking both data signals to an output signal, which is supplied to the decoder device (15). The invention also relates to a method enabling clock recovery and data retiming of HDB3 coded data signals.

IPC 1-7

H04L 25/49

IPC 8 full level

H04L 25/48 (2006.01); **H04L 25/49** (2006.01)

CPC (source: EP)

H04L 25/4925 (2013.01)

Citation (search report)

See references of WO 9854874A1

Designated contracting state (EPC)

CH DE ES FR GB IT LI SE

DOCDB simple family (publication)

DE 19722302 A1 19981203; EP 0985303 A1 20000315; WO 9854874 A1 19981203

DOCDB simple family (application)

DE 19722302 A 19970528; DE 9801226 W 19980505; EP 98933436 A 19980505