

Title (en)
Multilayer type chip inductor

Title (de)
Mehrschichtige Chip-Induktivität

Title (fr)
Inductance de puce multicouche

Publication
EP 0991088 A1 20000405 (EN)

Application
EP 99119524 A 19991001

Priority
KR 19980041680 A 19981002

Abstract (en)
A multilayer type chip inductor having a pair of outermost sheets and a plurality of intermediate sheets stacked between the outermost sheets, includes a pair of shielding sheets each of which has a shielding pattern for preventing electromagnetic waves generated thereby from being emitted. <IMAGE>

IPC 1-7
H01F 17/00; **H01F 27/36**

IPC 8 full level
H01F 17/00 (2006.01); **H01F 27/36** (2006.01)

CPC (source: EP KR US)
H01F 17/00 (2013.01 - KR); **H01F 17/0013** (2013.01 - EP); **H01F 27/36** (2013.01 - EP); **H01F 27/363** (2020.08 - EP KR US)

Citation (search report)
• [X] US 4916582 A 19900410 - OKAMURA HISATAKE [JP], et al
• [A] US 5250923 A 19931005 - USHIRO TOMOAKI [JP], et al
• [A] US 5392019 A 19950221 - OHKUBO AKIRA [JP]

Cited by
CN106935360A; EP1304707A3; US2015316937A1; US6847282B2; US7026904B2

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
EP 0991088 A1 20000405; KR 100279729 B1 20010302; KR 20000024888 A 20000506

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