

Title (en)

A METHOD FOR HANDLING CONDITIONAL JUMP INSTRUCTIONS IN A DATA PROCESSOR

Title (de)

EIN VERFAHREN ZUM BEARBEITEN VON SPRUNGBEFEHLEN IN EINEM DATEN-PROZESSOR

Title (fr)

PROCEDE DE GESTION D'INSTRUCTIONS DE SAUTS CONDITIONNELS DANS UN PROCESSEUR DE DONNEES

Publication

EP 0998701 A2 20000510 (EN)

Application

EP 98934048 A 19980707

Priority

- SE 9801334 W 19980707
- SE 9702762 A 19970721

Abstract (en)

[origin: WO9904335A2] The present invention relates to a method of handling conditional jump instructions in a computer processor (1). Space is allocated in a so-called instruction buffer (3) for respective instructions read into the processor. These spaces are given an order which corresponds to the order in which the instructions were read-in sequentially. The last position in the instruction buffer constitutes a read-out position (4). The results obtained when processing respective instructions can be saved in spaces allocated to these instructions in the instruction buffer (3), from which the results can be finally read-out from the read-out position (4).

IPC 1-7

G06F 9/38

IPC 8 full level

G06F 9/32 (2006.01); **G06F 9/38** (2006.01)

CPC (source: EP KR US)

G06F 9/30058 (2013.01 - EP KR US); **G06F 9/3804** (2013.01 - EP); **G06F 9/3806** (2013.01 - KR); **G06F 9/3861** (2013.01 - EP KR); **G06F 9/3885** (2013.01 - EP)

Designated contracting state (EPC)

DE FI FR GB IE

DOCDB simple family (publication)

WO 9904335 A2 19990128; **WO 9904335 A3 19990408**; AU 8365298 A 19990210; BR 9810768 A 20000815; CN 1271434 A 20001025; EP 0998701 A2 20000510; JP 2001510916 A 20010807; KR 20010022065 A 20010315; SE 510295 C2 19990510; SE 9702762 D0 19970721; SE 9702762 L 19990122

DOCDB simple family (application)

SE 9801334 W 19980707; AU 8365298 A 19980707; BR 9810768 A 19980707; CN 98809339 A 19980707; EP 98934048 A 19980707; JP 2000503482 A 19980707; KR 20007000634 A 20000120; SE 9702762 A 19970721