

Title (en)

VOLTAGE REGULATING CIRCUIT FOR ELIMINATING "LATCH-UP

Title (de)

SPANNUNGSREGELUNGSSCHALTUNG ZUR UNTERDRÜCKUNG DES LATCH-UP PHÄNOMENS

Title (fr)

CIRCUIT DE REGULATION DE TENSION DESTINE A SUPPRIMER UN PHENOMENE DIT "LATCH-UP"

Publication

EP 1010048 B1 20020502 (FR)

Application

EP 98929294 A 19980511

Priority

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- EP 97107722 A 19970512

Abstract (en)

[origin: EP0878752A1] The voltage regulator has a series bipolar transistor (2) to adjust the voltage output. The base voltage of the transistor is controlled by a resistor (5) and Zener diode (6) connected across the unregulated potential source, with the base connected to the junction between the resistor and the Zener diode. In addition, a voltage detector circuit (11) is connected across the regulated output and is also connected to the base of the transistor. The voltage detector causes the transistor to switch to the blocking state when latch up occurs. Filter capacitors (3,9) are fitted across the unregulated and the regulated supplies.

IPC 1-7

G05F 1/00; **G05F 1/575**

IPC 8 full level

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