

Title (en)

CIRCUIT WITH A MICROPROCESSOR AND A PUSHDOWN STORAGE

Title (de)

SCHALTUNGSANORDNUNG MIT EINEM MIKROPROZESSOR UND EINEM STAPELSPEICHER

Title (fr)

CIRCUIT COMPRENANT UN MICROPROCESSEUR ET UNE MEMOIRE A LISTE RENVERSEE

Publication

EP 1010081 A1 20000621 (DE)

Application

EP 97910244 A 19970930

Priority

- DE 9702253 W 19970930
- DE 19640316 A 19960930

Abstract (en)

[origin: DE19640316A1] The microprocessor has a logical address range (3) that is directly addressable through address outputs (2). The pushdown (4) is placed at least partially outside the logical address range (3). Advantage: The working storage (10) remaining in the logical address range (3) can be maximized.

IPC 1-7

G06F 12/06; **G06F 7/00**

IPC 8 full level

G06F 12/00 (2006.01); **G06F 12/06** (2006.01)

CPC (source: EP KR)

G06F 12/06 (2013.01 - KR); **G06F 12/0623** (2013.01 - EP)

Citation (search report)

See references of WO 9814876A1

Designated contracting state (EPC)

AT CH DE ES FR GB IT LI

DOCDB simple family (publication)

DE 19640316 A1 19980402; BR 9712154 A 19990831; CN 1232564 A 19991020; EP 1010081 A1 20000621; JP 2000503792 A 20000328; KR 20000048754 A 20000725; WO 9814876 A1 19980409

DOCDB simple family (application)

DE 19640316 A 19960930; BR 9712154 A 19970930; CN 97198398 A 19970930; DE 9702253 W 19970930; EP 97910244 A 19970930; JP 51614898 A 19970930; KR 19997002740 A 19990330