

Title (en)
Multilayer type chip inductor

Title (de)
Mehrschichtige Chip-Induktivität

Title (fr)
Inductance de puce multicouche

Publication
EP 1011116 A3 20010509 (EN)

Application
EP 99114704 A 19990727

Priority
KR 19980055594 A 19981217

Abstract (en)
[origin: EP1011116A2] An inventive multilayer type chip inductor includes a pair of outermost sheets having a terminal pattern, respectively, and a plurality of intermediate sheets stacked between the outermost sheets. Each of the intermediate sheets has one turned coil-shaped conductor pattern in an electric contact with the terminal pattern of the outermost sheet. Each of the conductor patterns is spirally and outwardly turned from a central portion of the intermediate sheet. <IMAGE>

IPC 1-7
H01F 27/28; **H01F 17/00**

IPC 8 full level
H01F 17/00 (2006.01); **H01F 27/28** (2006.01)

CPC (source: EP KR)
H01F 17/00 (2013.01 - KR); **H01F 27/2804** (2013.01 - EP)

Citation (search report)

- [XA] US 5302932 A 19940412 - PERSON HERMAN R [US], et al
- [XA] FR 2379229 A1 19780825 - EUROFARAD [FR]
- [PX] WO 9909568 A1 19990225 - DALE ELECTRONICS [US]
- [A] PATENT ABSTRACTS OF JAPAN vol. 015, no. 161 (E - 1060) 23 April 1991 (1991-04-23)
- [A] PATENT ABSTRACTS OF JAPAN vol. 1995, no. 05 30 June 1995 (1995-06-30)

Designated contracting state (EPC)
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)
EP 1011116 A2 20000621; **EP 1011116 A3 20010509**; KR 20000040049 A 20000705

DOCDB simple family (application)
EP 99114704 A 19990727; KR 19980055594 A 19981217