

Title (en)

FLAT SEMICONDUCTOR DEVICE AND POWER CONVERTER EMPLOYING THE SAME

Title (de)

FLACHE HALBLEITERANORDNUNG UND STROMRICHTER MIT DERSELBEN

Title (fr)

DISPOSITIF PLAT A SEMI-CONDUCTEUR ET CONVERTISSEUR DE COURANT UTILISANT CE DERNIER

Publication

EP 1014451 A4 20001115 (EN)

Application

EP 97914542 A 19970326

Priority

JP 9701011 W 19970326

Abstract (en)

[origin: WO9843301A1] Control electrode wirings which are led out from control electrodes over a number of chips built in a flat package and insulating members which are provided in order to insulate the control electrode wirings from main electrode wirings are also given functions of positioning of the respective semiconductor chips in the flat package. Further, a one-piece control electrode wiring network is housed in the common electrodes of the package and the electrodes which are led out from the control electrodes of the respective semiconductor chips are connected to the network to simplify the processing of a large number of gate signal wirings.

IPC 1-7

H01L 25/07; H01L 29/78

IPC 8 full level

H01L 25/07 (2006.01); **H01L 25/11** (2006.01)

CPC (source: EP US)

H01L 25/072 (2013.01 - EP US); **H01L 25/112** (2013.01 - EP US); **H01L 2924/0002** (2013.01 - EP US)

Citation (search report)

- [X] EP 0702406 A2 19960320 - TOSHIBA KK [JP]
- See references of WO 9843301A1

Cited by

DE102004018477B4; EP1378941A3; DE10058446B4; DE10058446B8; GB2332092B; EP1378938A3; US6873045B2; US11270983B2; WO2020081448A1; US7193317B2; US7508068B2

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

WO 9843301 A1 19981001; EP 1014451 A1 20000628; EP 1014451 A4 20001115; US 6452261 B1 20020917

DOCDB simple family (application)

JP 9701011 W 19970326; EP 97914542 A 19970326; US 38064599 A 19990907