

Title (en)

DESIGN RULE CHECKING SYSTEM AND METHOD

Title (de)

METHODE UND SYSTEM ZUR KONTROLLE VON ENTWURFSREGELN

Title (fr)

PROCEDE ET SYSTEME DE CONTROLE DE REGLES DE CONCEPTION

Publication

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Application

EP 98951922 A 19980917

Priority

- US 9819510 W 19980917
- US 5930697 P 19970917
- US 93192197 A 19970917
- US 6954997 P 19971212
- US 13099698 A 19980807
- US 15439798 A 19980916
- US 15378398 A 19980916

Abstract (en)

[origin: WO9914638A1] A method for performing design rule checking on an optical proximity correction (OPC) corrected or otherwise corrected designs is described. The corrected design is accessed to generate a simulated image (2010). The simulated image corresponds to a simulation of an image which would be printed on a wafer if the wafer was exposed to an illumination source directed through the corrected design (2020). The characteristics of the illumination source are determined by a set of lithography parameters. In creating the image, additional characteristics can be used to simulate portions of the fabrication process. The simulated image can then be used by the design rule checker. Importantly, the simulated image can be processed to reduce the number of vertices in the simulated image, relative to the number of vertices in the OPC corrected design layout (2020). Also, the simulated image can be compared with an ideal layout image, the results of which can then be used to reduce the amount of information that is needed to perform the design rule checking.

IPC 1-7

G03F 9/00; G06F 17/00; G06F 17/50

IPC 8 full level

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