

Title (en)
Addressable matrix arrays

Title (de)
Adressierbare Matrixgeräte

Title (fr)
Dispositifs à matrice adressable

Publication
EP 1026657 A2 20000809 (EN)

Application
EP 00300444 A 20000121

Priority
GB 9902343 A 19990204

Abstract (en)
A ferroelectric liquid crystal display panel 10 comprises a layer of ferroelectric liquid crystal material contained between two substrates and bearing first and second electrode structures on their inside surfaces. The first and second electrode structures comprise respectively a series of row and column electrode tracks 4 and 5 which cross one another to form a matrix array of switching elements. The addressing of the switching elements is controlled by a data signal generator 14 and a strobe signal generator 15 by applying data waveforms in parallel to the column electrode tracks 41, 42..4n and by sequentially applying a strobe waveform to the row electrode tracks 51, 52..5m so as to switch selected switching elements along each row from one state to another. In order to compensate for differential waveform distortion across the display due to the effects of the different electrode track resistances seen at each column driver input, the data signal generator 14 is coupled to the column electrode tracks by compensating resistances R1, R2, R3, etc. having resistance values which vary from the first to the last columns. It is therefore possible to substantially equalise the picture quality across the display, and to ensure that temperature variations across the display caused by the different power components of the different waveforms are substantially equalised. <IMAGE>

IPC 1-7
G09G 3/36; G09G 3/20

IPC 8 full level
G09G 3/36 (2006.01); **G02F 1/133** (2006.01); **G02F 1/1343** (2006.01); **G09F 9/35** (2006.01); **G09G 3/20** (2006.01)

CPC (source: EP)
G09G 3/3629 (2013.01); **G09G 3/3692** (2013.01); **G09G 3/2051** (2013.01); **G09G 2300/06** (2013.01); **G09G 2320/0233** (2013.01); **G09G 2320/041** (2013.01)

Cited by
EP1221685A3; FR2916296A1; US7019719B2; US8941640B2

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
EP 1026657 A2 20000809; **EP 1026657 A3 20020619**; GB 9902343 D0 19990324; JP 2000227584 A 20000815

DOCDB simple family (application)
EP 00300444 A 20000121; GB 9902343 A 19990204; JP 2000028402 A 20000204