Title (en)

Planar filter

Title (de)

Planarfilter

Title (fr)

Filtre planaire

Publication

EP 1028497 A3 20010808 (DE)

Application

EP 99125868 A 19991224

Priority

DE 29902505 U 19990202

Abstract (en)

[origin: CA2293131A1] In order to suppress parasitic capacitances between the signal electrodes in a planar filter with many signal pins, which are led through a carrier (I) and which each have a capacitor, with a signal layer (3) connected to the signal pin, a ground layer (4) connected to ground and a dielectric layer separating the two said layers, said planar filter is of a monolithic construction. The electrodes of the capacitors are applied to the carrier (1), which forms the dielectric, is shaped as a block from a mass of a higher dielectric constant and, after shaping and perforation, is sintered and ground, the ground electrode (4) covering the entire surface area of one of the side surfaces of the carrier (1), apart from the pin leadthroughs (2) of the signal pins and their surrounding area, and the signal electrodes (3) on the other side surface of the carrier (1) forming insular regions extending from the pin lead-throughs (2) of the signal pins to the edge of the carrier (1). This planar filter, advantageously provided with a supporting plate, is used for example in plug-in connectors soldered onto printed circuit boards.

IPC 1-7

H01R 13/719

IPC 8 full level

H01R 13/719 (2011.01)

CPC (source: EP)

H01R 13/7195 (2013.01)

Citation (search report)

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EP1244341A3

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DE 29902505 U1 20000323; CA 2293131 A1 20000802; CA 2293131 C 20080318; DE 59911042 D1 20041216; EP 1028497 A2 20000816; EP 1028497 A3 20010808; EP 1028497 B1 20041110

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