

Title (en)

APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS

Title (de)

VIRTUELLE NETZWERK-SCHNITTSTELLE FÜR UND ZWISCHEN INTEGRIERTEN UND PRIMÄR-RECHNERN

Title (fr)

INTERFACE DE RESEAU APPARENTE DESTINEE A RELIER DES PROCESSEURS INTEGRE ET HOTE

Publication

**EP 1032884 A1 20000906 (EN)**

Application

**EP 98958588 A 19981112**

Priority

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- US 6658397 P 19971126
- US 3041198 A 19980225

Abstract (en)

[origin: WO9927456A1] An apparent network interface (30) permits an embedded processor (20) to communicate to a host processor (2) using standard network communication mechanisms/protocols such as TCP/IP, NFS, FTP, HTTP, etc. The web server protocol HTTP is particularly useful because it permits the embedded computer to publish a user interface for remote monitoring and remote control using a standard web browser application. The invention provides the host computer (10) with an apparent network interface (30) that appears to be a standard network device, such as an Ethernet interface card. This apparent network interface (30) communicates directly with the embedded processor (20) which appears to be a device on the apparent network. Significant cost savings and performance enhancements are realized by implementing the communication directly over the host computer's peripheral bus (6) rather than using standard network hardware such as Ethernet hardware.

IPC 1-7

**G06F 13/00**; **G06F 13/42**; **H04L 12/56**; **H04L 29/06**

IPC 8 full level

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