

Title (en)  
MEMORY APPARATUS INCLUDING PROGRAMMABLE NON-VOLATILE MULTI-BIT MEMORY CELL, AND APPARATUS AND METHOD FOR DEMARCATING MEMORY STATES OF THE CELL

Title (de)  
SPEICHERVORRICHTUNG MIT PROGRAMMIERBARER NICHT-FLÜCHTIGER MULTI-BIT-SPEICHERZELLE SOWIE VORRICHTUNG UND VERFAHREN ZUR SPEICHERZUSTANDSABGRENZUNG DER ZELLE

Title (fr)  
DISPOSITIF MEMOIRE COMPORTANT DES CELLULES DE MEMOIRE MULTI-BITS, NON VOLATILE, PROGRAMMABLE, ET APPAREIL ET PROCEDE PERMETTANT DE DELIMITER LES ETATS DE MEMOIRE D'UNE CELLULE

Publication  
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Application  
**EP 97952391 A 19971211**

Priority  
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• US 97591997 A 19971121

Abstract (en)  
[origin: WO9927539A1] Memory states of a multi-bit memory cell are demarcated by generating read reference signals having levels that constitute boundaries of the memory states. The read reference signals are dependent upon the levels of programming reference signals used for controlling the programming of the memory cell. The memory cell can thus be programmed without reading out its memory state during the programming process, with programming margins being assured by the dependence of the read reference signals on the programming reference signals. Both sets of reference signals may be generated by reference cells which track variations in the operating characteristics of the memory cell with changes in conditions, such as temperature and system voltages, to enhance the reliability of memory programming and readout.

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IPC 8 full level  
**G11C 16/06** (2006.01); **G11C 11/56** (2006.01); **G11C 16/02** (2006.01); **G11C 16/04** (2006.01)

CPC (source: EP)  
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